

**AMENDMENTS TO THE SPECIFICATION:**

Please replace paragraph [0030] with the following amended paragraph:

[0030] Figure 3 is a block diagram showing a data transaction system 300 for memory access using a port independent data transaction interface in accordance with an embodiment of the present invention. The data transaction system 300 includes a multi-port memory controller 302 having an initiator 304 and a data transfer storage device ~~201~~ 304. The multi-port memory controller 302 controls access to a memory device 308. Coupled to each port of the multi-port memory controller 302 is a source device 306a-306c. As mentioned above the embodiments of the present invention allow fully pipelined data processing for multi-client devices. The data transaction interface of the embodiments of the present invention allows decoupling of data and corresponding commands, and further allows transactions to have different priorities, thus allowing higher priority transactions to interrupt prior received lower priority transactions.